

### **Amendments to the Specification**

Please amend the specification as follows:

On Page 4, please amend paragraph [0012] as follows

[0012] FIG. 1 is a block diagram of one embodiment of a computer system. In one embodiment, the computer system may include a processor 101 or set of processors for processing instructions and executing programs. In one embodiment, processor 101 may be in communication with a hub 107. Hub 107 may facilitate communication between processor 101, system memory 113, graphics processor 109 and similar devices. The use of system memory 113 is further illustrated in FIG. 2 as discussed below. In one embodiment, hub 107 is a component or chipset on a mainboard or similar platform. Hub 107 may be a "Northbridge" chipset. In one embodiment, graphics processor 109 may be a component or chipset on a mainboard or similar platform. In another embodiment, graphics processor 109 may be on a peripheral card connected to the mainboard or platform via an accelerated graphics port (AGP) or similar connection. Graphics processor 109 may be in communication with a monitor 111 or display device. A display device may be a cathode ray tube (CRT) device, liquid crystal display device, plasma display device or similar display device.

On Page 4, please amend paragraph [0013] as follows

[0013] In one embodiment, hub 107 may be in communication with input output (I/O) hub 115. I/O hub 115 may facilitate communication between hub 107 and I/O devices, such as storage devices including fixed storage devices, removable media devices, DVD drive 117 that reads DVD media 131 and similar devices. In one embodiment, I/O hub 115 may be a component or chipset on a mainboard or similar platform. I/O hub 115 may be a "Southbridge" chipset. I/O hub 115 may be in communication with a sound card 121. Sound card 121 may generate an audio signal to output to a speaker 125 or similar device. In one embodiment, an integrated audio controller may be used in place of or with a sound card. Hub 107 includes a DMA controller to

obfuscate data locations by control of access to a data buffer and description list table, as discussed further in relation to FIG. 3 below.

On Page 10, please amend paragraph [0028] as follows

[0028]       The obfuscation system may be implemented in software, for example, in a simulator, emulator or similar software. A software implementation may include a microcode implementation. A software implementation may be stored on a machine readable medium. A "machine readable" medium may include any medium that can store or transfer information. Examples of a machine readable medium include a ROM, a floppy diskette, a CD-ROM, an optical disk, a hard disk, ~~a radio frequency (RF) link,~~ and similar media and mediums.